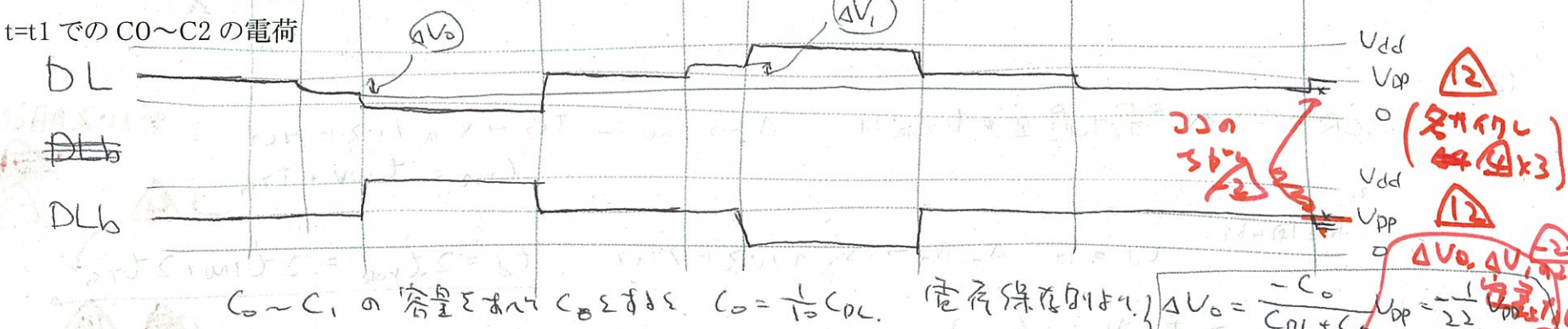
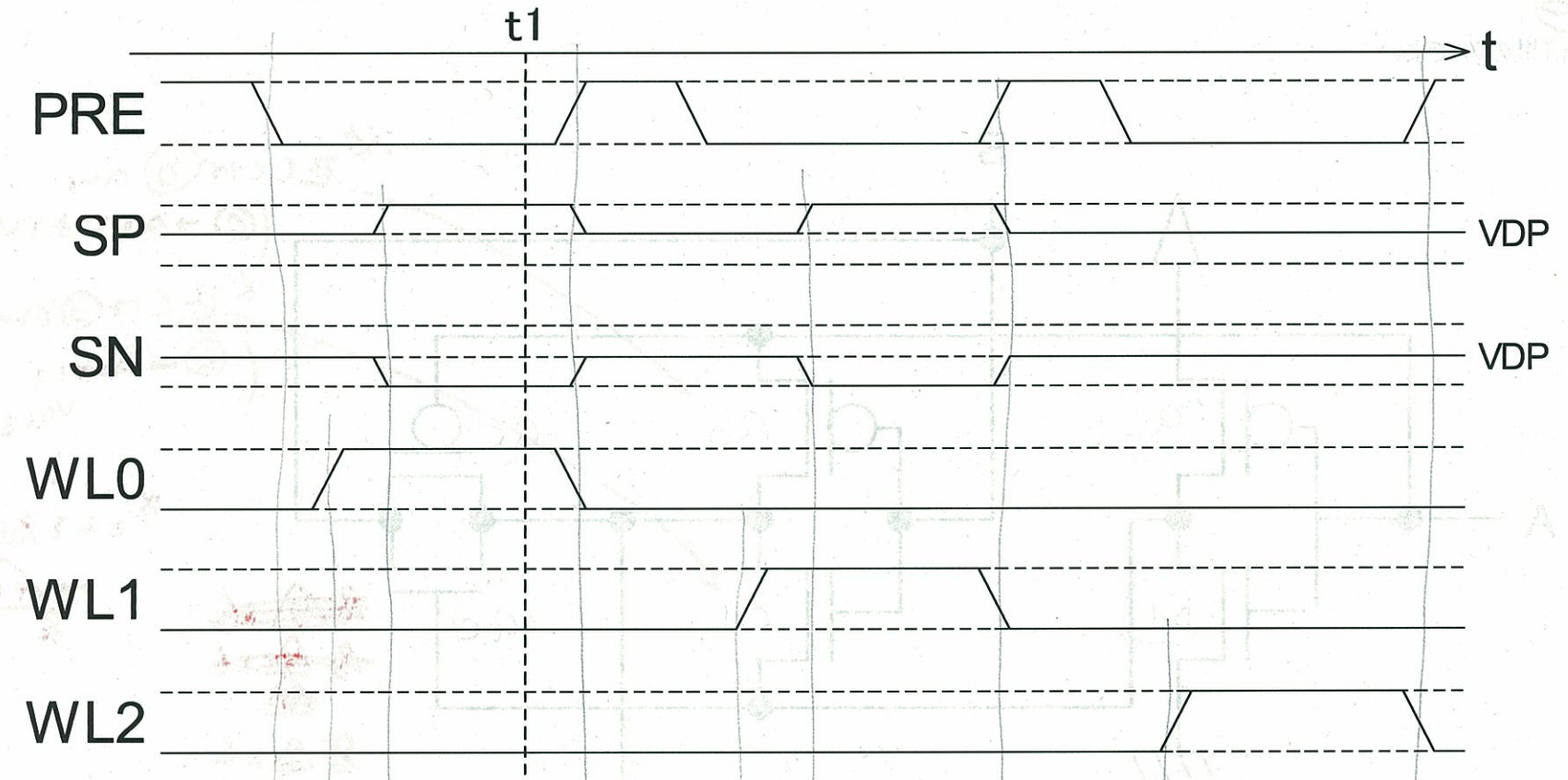


2. ※結果のみでよい 30



3. 20 並列乗算器

DRAM / SRAM / ROM.
~~SRAM~~
 容量 PLI 型 DAC
 理由 短長 $(15 + 15) \times 2$
 相対精度が高い

Handwritten notes and equations in red ink:

- $\Delta V_0 = \frac{-C_0}{C_{DL} + C_0} V_{PP} = \frac{-1}{22} V_{PP}$
- $\Delta V_1 = \frac{C_0}{C_{DL} + C_0} V_{PP} = \frac{1}{22} V_{DD}$
- Notes: "3.3V 5.0V", "12V", "5.0V", "12V".

4. 10

Handwritten notes:

- 70% ± 5% + 70% ± 5%
- A diagram with two triangles and a plus sign: $\triangle + \triangle$

Handwritten equations and notes:

$$C_{DL} \cdot V_{PP} = (C_{DL} + C_0)(V_{PP} + V_0)$$

$$C_{DL} \cdot V_{PP} + C_0 \cdot V_{DD} = (C_{DL} + C_0)(V_{PP} + V_1)$$

$$t = t_1 \sim t_2$$

$$C_0 \sim C_2 \text{ の電荷は}$$

- $C_0 : 0$ 2
- $C_1 : C_0 \cdot V_{DD}$ 2
- $C_2 : 0$ 2

5. 10

見本 A6 に 10