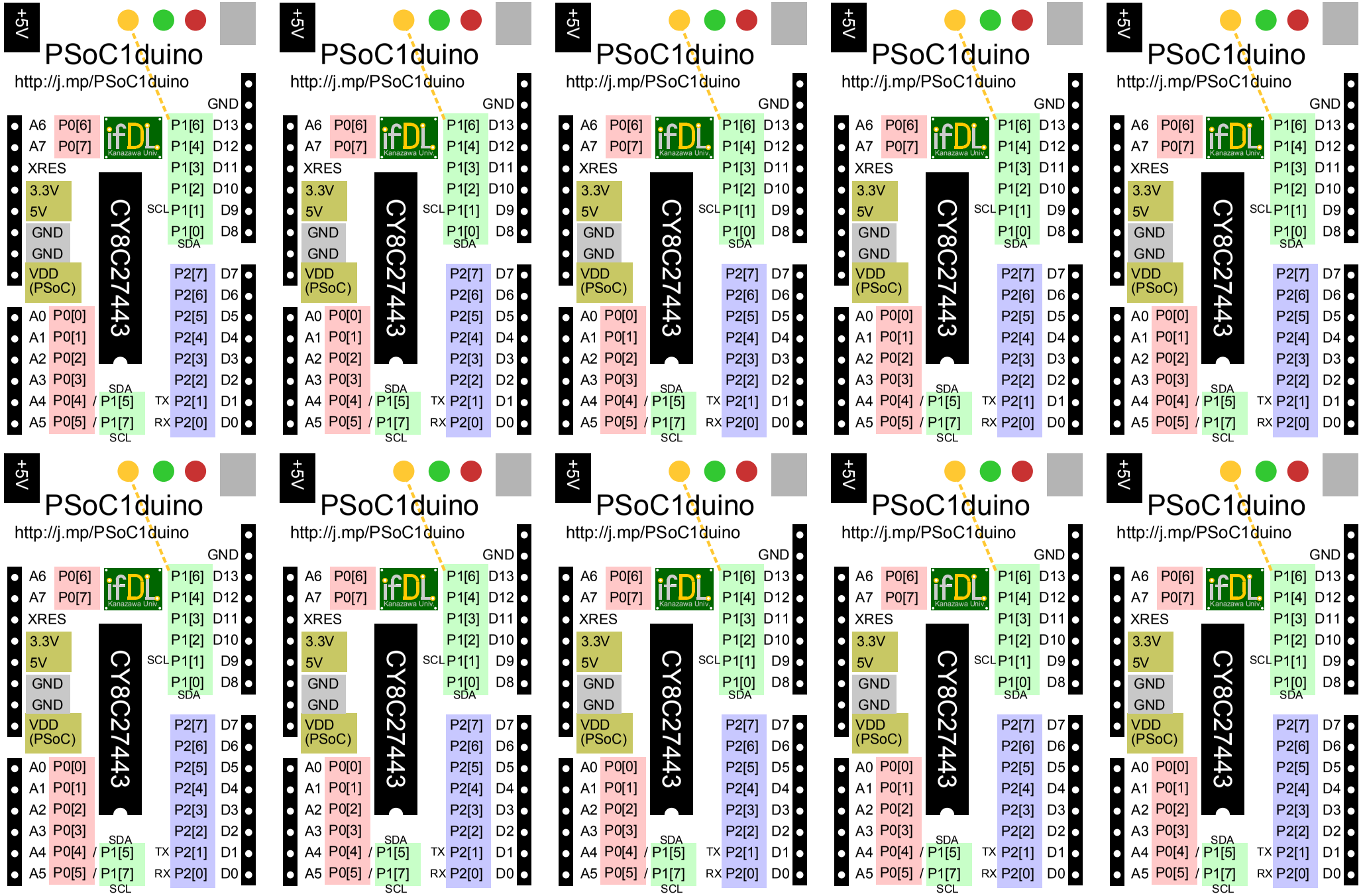
+5V
PSoC1duino
http://j.mp/PSoC1duino
ifDL
Kanazawa Univ.
CY8C27443
GND
A6 P0[6]
A7 P0[7]
XRES
3.3V
5V
GND
GND
VDD (PSoC)
A0 P0[0]
A1 P0[1]
A2 P0[2]
A3 P0[3]
A4 P0[4] / P1[5] SDA
A5 P0[5] / P1[7] SCL
P1[6] D13
P1[4] D12
P1[3] D11
P1[2] D10
SCL P1[1] D9
P1[0] SDA D8
P2[7] D7
P2[6] D6
P2[5] D5
P2[4] D4
P2[3] D3
P2[2] D2
TX P2[1] D1
RX P2[0] D0